

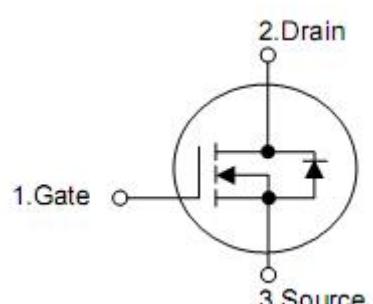
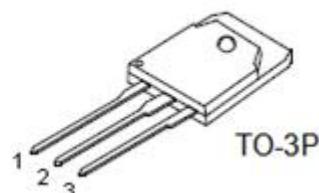
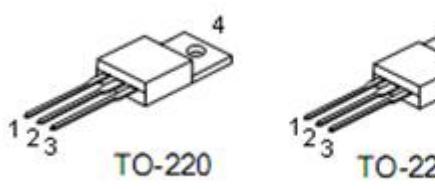
1. General Features

- Proprietary New Planar Technology
- $R_{DS(ON),typ.}=0.24\Omega @ V_{GS}=10V$
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

2. Applications

- Adaptor Charger
- SMPS Power Supply
- LCD Panel Power

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

4. Ordering Information

Part Number	Package	Brand
KNP7150A	TO-220	KIA
KNF7150A	TO-220F	KIA
KNH7150A	TO-3P	KIA

5. Absolute maximum ratings

(T_c= 25 °C , unless otherwise specified)

Symbol	Parameter	Ratings			Unit
		TO-3P	TO-220	TO-220F	
V _{DSS}	Drain-to-Source Voltage ^[1]	500			V
V _{GSS}	Gate-to-Source Voltage	±30			
I _D	Continuous Drain Current	20			A
	Continuous Drain Current@ T _c =100 °C	Figure3			
I _{DM}	Pulsed Drain Current at V _{GS} =10V ^[2]	Figure6			mJ
E _{AS}	Single Pulse Avalanche Energy	1500			
dv /dt	Peak Diode Recovery dv/dt ^[3]	5.0			V/ns
P _D	Power Dissipation	275	175	60	W
	Derating Factor above 25 °C	2.2	1.40	0.48	W/ °C
T _L T _{PAK}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	300	260		°C
	Operating and Storage Temperature Range	-55 to 150			

6. Thermal characteristics

Symbol	Parameter	Ratings			Unit
		TO-3P	TO-220	TO-220F	
R _{θJC}	Thermal Resistance, Junction-to-Case	0.45	0.71	2.08	°C
R _{θJA}	Thermal Resistance, Junction-to-Ambient	60	62	100	/W

7. Electrical characteristics

OFF Characteristics		(TJ=25°C,unless otherwise specified)				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-to-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	500	--	--	V
I _{DSS}	Drain-to-Source Leakage Current	V _{DS} =500V, V _{GS} =0V	--	--	1	uA
		V _{DS} =400V, V _{GS} =0V, T _J =125°C	--	--	100	
I _{GSS}	Gate-to-Source Leakage Current	V _{GS} =+30V, V _{DS} =0V	--	--	100	nA
		V _{GS} =-30V, V _{DS} =0V	--	--	-100	
ON Characteristics		(TJ=25°C,unless otherwise specified)				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
R _{DS(ON)}	Static Drain-to-Source On-Resistance ^[4]	V _{GS} =10V, I _D =10A	--	0.24	0.3	Ω
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2.0	--	4.0	V
g _{FS}	Forward Transconductance	V _{DS} =15V, I _D =10A	--	19	--	S
Dynamic Characteristics		Essentially independent of operating temperature				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, f=1.0MHZ	--	2650	--	pF
C _{oss}	Output Capacitance		--	255	--	
C _{rss}	Reverse Transfer Capacitance		--	34	--	
Q _g	Total Gate Charge	V _{DD} =250V, I _D =20A, V _{GS} =0 to 10V	--	65	--	nC
Q _{gs}	Gate-to-Source Charge		--	14	--	
Q _{gd}	Gate-to-Drain (Miller) Charge		--	24	--	
Resistive Switching Characteristics		Essentially independent of operating temperature				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t _{d(ON)}	Turn-on Delay Time	V _{DD} =250V, I _D =20A, V _{GS} = 10V R _G =25Ω	--	34	--	ns
t _{rise}	Rise Time		--	76	--	
t _{d(OFF)}	Turn-Off Delay Time		--	164	--	
t _{fall}	Fall Time		--	85	--	
Source-Drain Body Diode Characteristics		(T _J =25°C,unless otherwise specified)				
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Unit
I _{SD}	Continuous Source Current ^[4]	Integral PN-diode in MOSFET	--	--	20	A
I _{SM}	Pulsed Source Current ^[4]		--	--	80	
V _{SD}	Diode Forward Voltage	I _S =20A, V _{GS} =0V	--	--	1.5	V
t _{rr}	Reverse recovery time	V _{GS} =0V ,I _F =20A, diF/dt=100A/μs	--	310	--	ns
Q _{rr}	Reverse recovery charge		--	3.0	--	uC

Note:

1.T_J=+25°C to +150°C

2.Repetitive rating; pulse width limited by maximum junction temperature

3. I_{SD}= 20Adi/dt < 100 A/μs, V_{DD}< BV_{DSS}, T_J=+150°C.

4.Pulse width≤380μs; duty cycle≤2%

8. Test circuits and waveforms

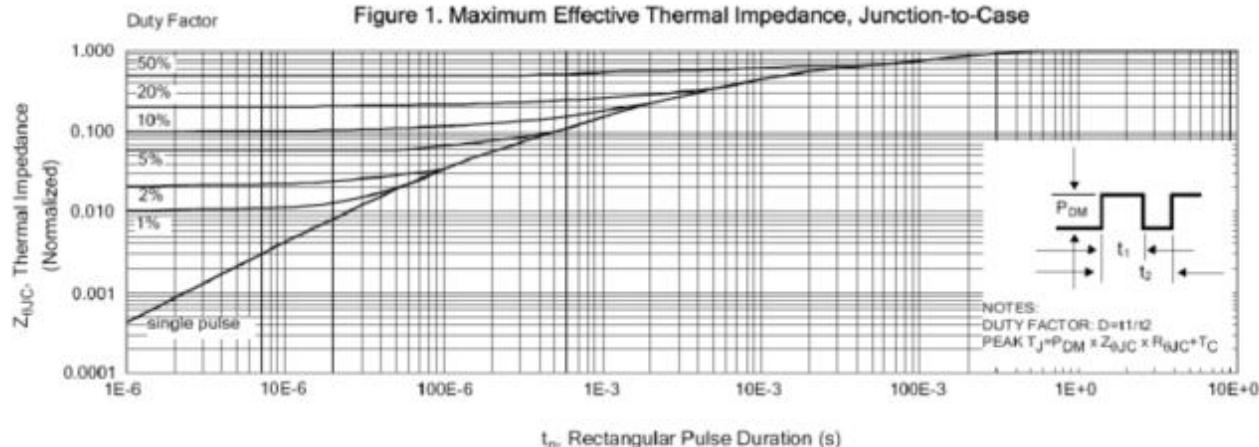


Figure 2. Maximum Power Dissipation vs Case Temperature

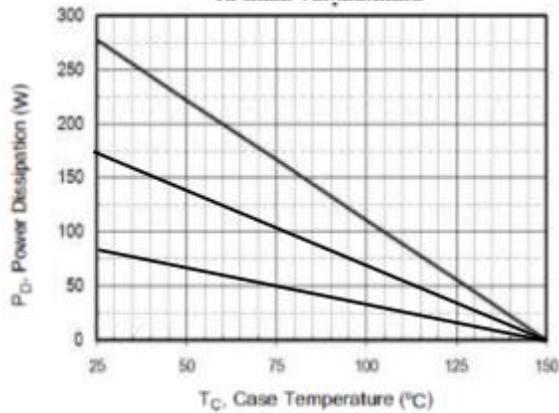


Figure 4. Typical Output Characteristics

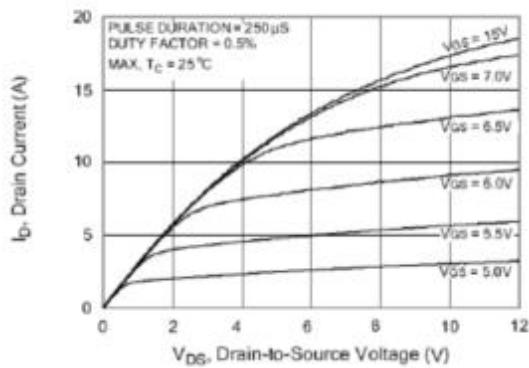


Figure 3. Maximum Continuous Drain Current vs Case Temperature

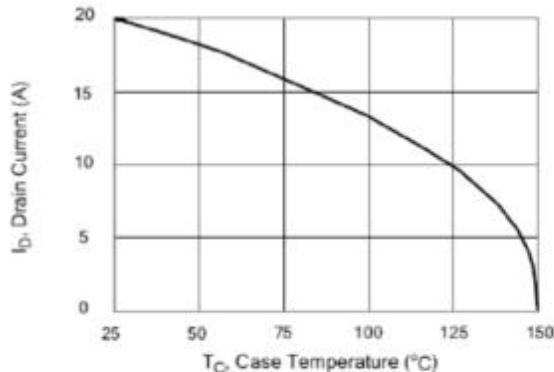


Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current

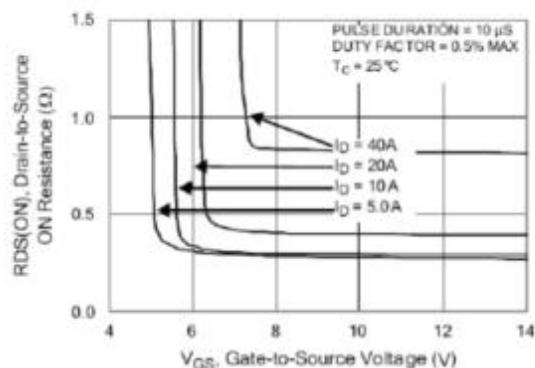


Figure 6. Maximum Peak Current Capability

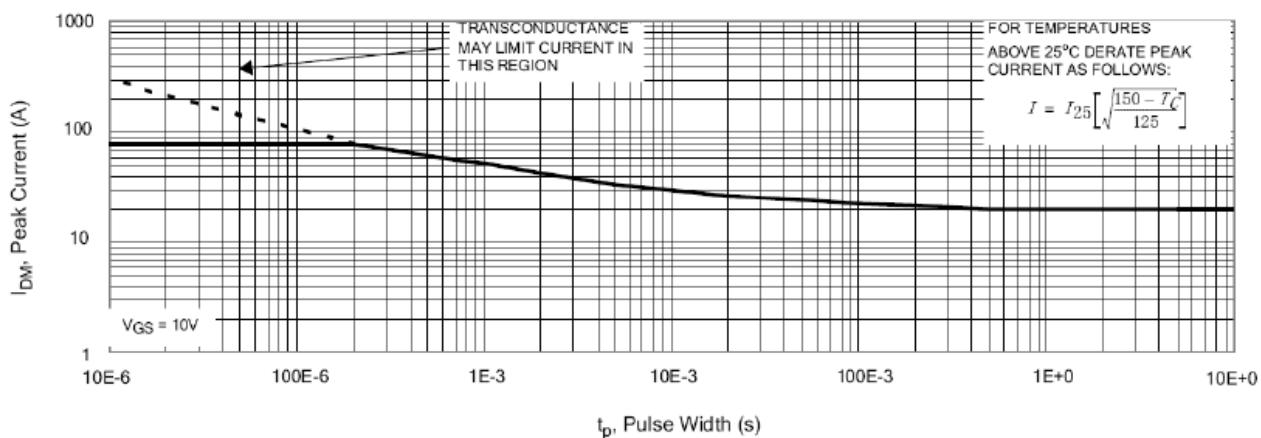


Figure 7. Typical Transfer Characteristics

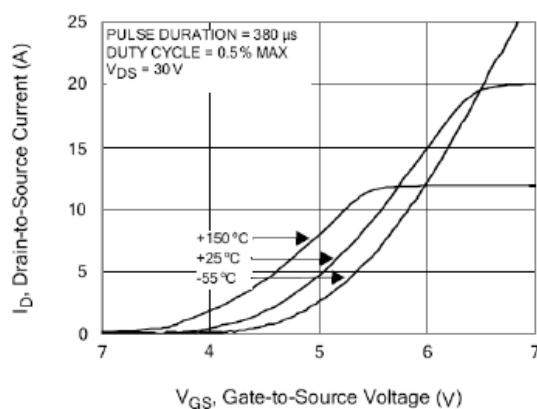


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

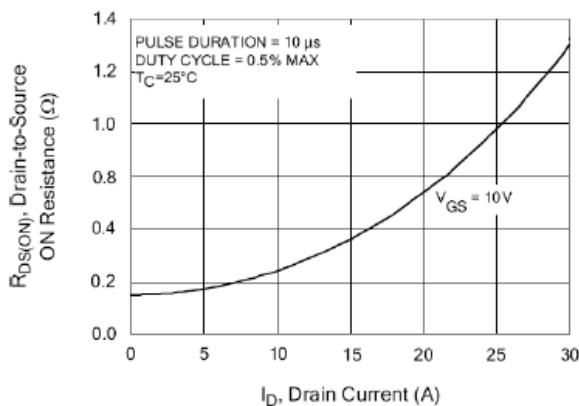


Figure 8. Unclamped Inductive Switching Capability

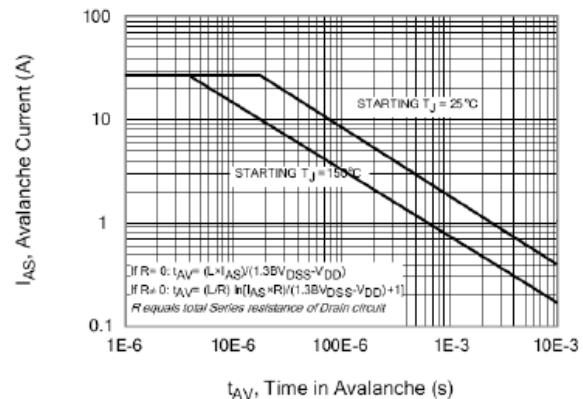
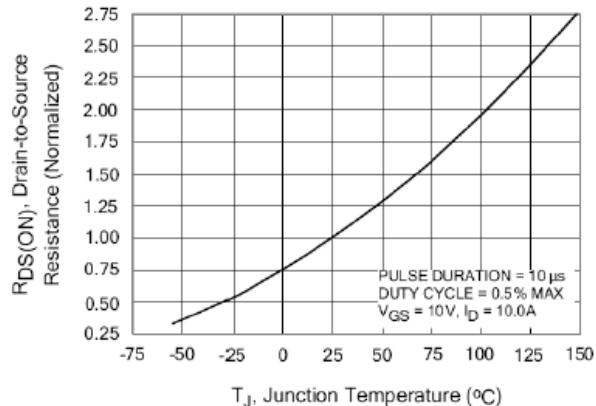


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature



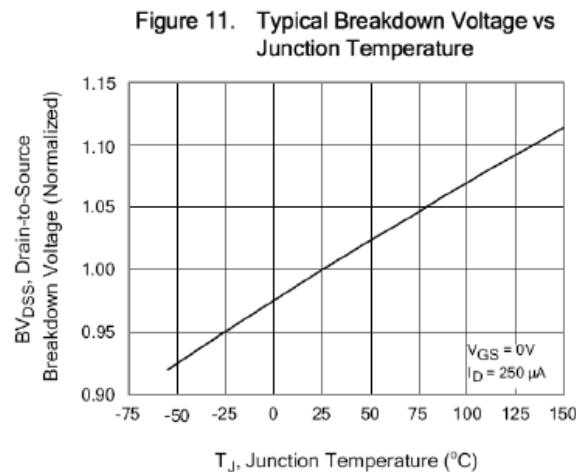


Figure 13. Maximum Forward Bias Safe Operating Area

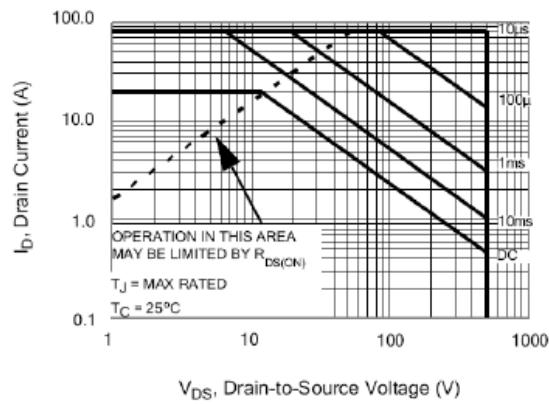


Figure 15. Typical Gate Charge vs Gate-to-Source Voltage

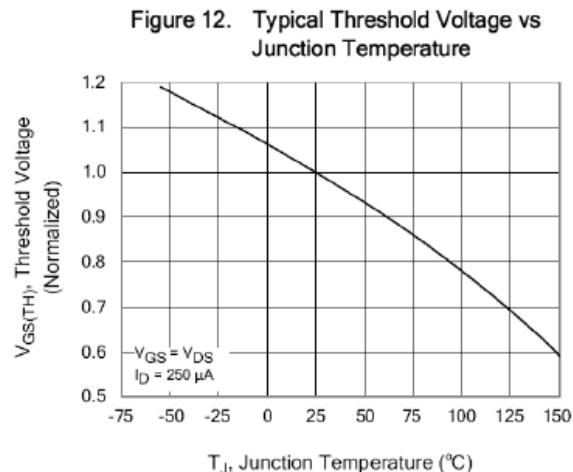
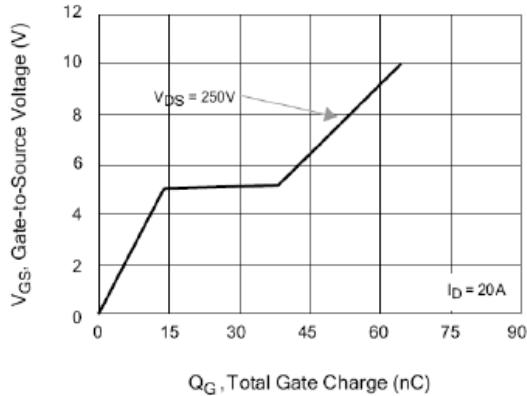


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

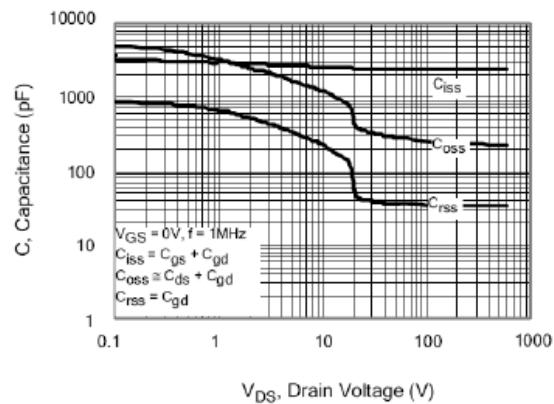


Figure 16. Typical Body Diode Transfer Characteristics

