

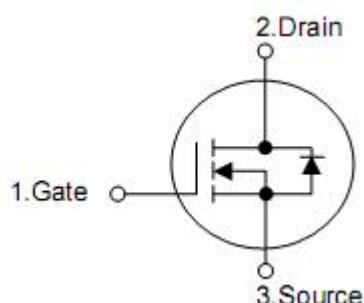
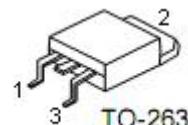
1. Features

- $R_{DS(on)}=4.0\text{m}\Omega$ (typ.) @ $V_{GS}=10\text{V}$
- 100% avalanche tested
- Reliable and rugged
- Lead free and green device available (RoHS Compliant)

2. Applications

- Switching application
- Power management for inverter systems

3. Symbol



| Pin | Function |
|-----|----------|
| 1 | Gate |
| 2 | Drain |
| 3 | Source |

4. Ordering Information

| Part Number | Package | Brand |
|-------------|---------|-------|
| KNB2808A | TO-263 | KIA |

5. Absolute maximum ratings

($T_A=25^\circ\text{C}$,unless otherwise noted)

| Parameter | | Symbol | Rating | Units |
|----------------------------------|-------------------------|-----------|------------|------------------|
| Drain-source voltage | | V_{DSS} | 80 | V |
| Gate-source voltage | | V_{GSS} | ± 25 | V |
| Maximum junction temperature | | T_J | 175 | $^\circ\text{C}$ |
| Storage temperature range | | T_{STG} | -55 to 175 | $^\circ\text{C}$ |
| Diode continuous forward current | $T_C=25^\circ\text{C}$ | I_S | 150 | A |
| Continuous drain current | $T_C=25^\circ\text{C}$ | I_D | 150 | A |
| | $T_C=100^\circ\text{C}$ | | 114 | A |
| Pulse drain current* | $T_C=25^\circ\text{C}$ | I_{DM} | 660** | A |
| Avalanche energy,single pulsed | $L=0.5\text{mH}$ | E_{AS} | 1.1*** | J |
| Maximum power dissipation | $T_C=25^\circ\text{C}$ | P_D | 178 | W |
| | $T_C=100^\circ\text{C}$ | | 89 | W |

Note:

* Repetitive rating;pulse width limited by junction temperature;

** Drain current is limited by junction temperature;

*** $V_D=64\text{V}$.

6. Thermal characteristics

| Parameter | Symbol | Rating | Unit |
|-------------------------------------|-----------------|--------|--------------------|
| Thermal resistance,Junction-ambient | $R_{\theta JA}$ | 62.5 | $^\circ\text{C/W}$ |
| Thermal resistance,Junction-case | $R_{\theta JC}$ | 0.7 | $^\circ\text{C/W}$ |

7. Electrical characteristics

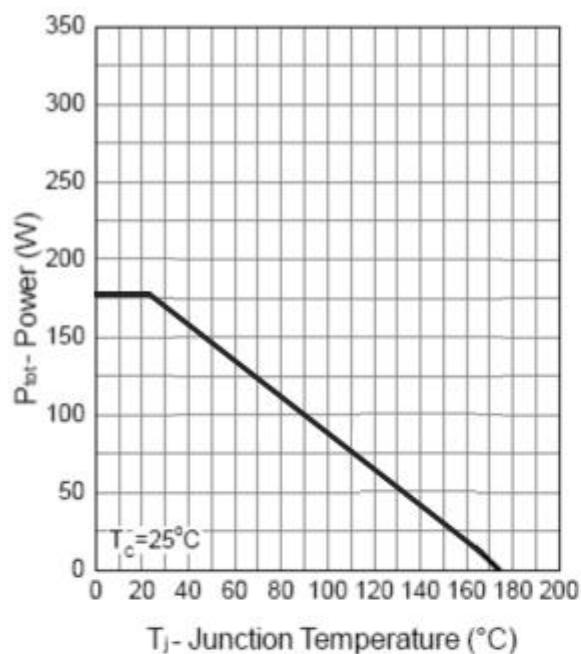
($T_A=25^\circ\text{C}$,unless otherwise noted)

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Units |
|----------------------------------|--------------------------|--------------------------------------------------------------------------------------------------|-----|------|-----------|------------------|
| Drain-source breakdown voltage | BV_{DSS} | $V_{\text{GS}}=0\text{V}, I_{\text{DS}}=250\mu\text{A}$ | 80 | - | - | V |
| Zero gate voltage drain current | I_{DSS} | $V_{\text{DS}}=80\text{V}, V_{\text{GS}}=0\text{V}$ | - | - | 1 | μA |
| | | $T_J=85^\circ\text{C}$ | - | - | 10 | |
| Gate threshold voltage | $V_{\text{GS(th)}}$ | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$ | 2.0 | 3.0 | 4.0 | V |
| Gate leakage current | I_{GSS} | $V_{\text{GS}}=\pm 25\text{V}, V_{\text{DS}}=0\text{V}$ | - | - | ± 100 | nA |
| Drain-source on-state resistance | $R_{\text{DS(on)}}^*$ | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=40\text{A}$ | - | 4.0 | 5.0 | $\text{m}\Omega$ |
| Gate resistance | R_g | $V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$ | - | 1.8 | - | Ω |
| Diode forward voltage | V_{SD}^* | $I_{\text{SD}}=40\text{A}, V_{\text{GS}}=0\text{V}$ | - | 0.8 | 1.2 | V |
| Reverse recovery time | t_{rr} | $I_{\text{SD}}=85\text{A},$ $dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$ | - | 30 | - | nS |
| Reverse recovery charge | Q_{rr} | | - | 52 | - | nC |
| Input capacitance | C_{iss} | $V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$ | - | 6109 | - | pF |
| Output capacitance | C_{oss} | | - | 995 | - | |
| Reverse transfer capacitance | C_{rss} | | - | 530 | - | |
| Turn-on delay time | $t_{\text{d(on)}}$ | $V_{\text{DD}}=40\text{V}, I_{\text{DS}}=85\text{A},$ $R_g=6\Omega, V_{\text{GS}}=10\text{V}$ | - | 28 | - | ns |
| Rise time | t_r | | - | 18 | - | |
| Turn-off delay time | $t_{\text{d(off)}}$ | | - | 42 | - | |
| Fall time | t_f | | - | 54 | - | |
| Total gate charge | Q_g | $V_{\text{DS}}=64\text{V}, V_{\text{GS}}=10\text{V}$ $I_{\text{DS}}=85\text{A}$ | - | 152 | - | nC |
| Gate-source charge | Q_{gs} | | - | 25 | -- | |
| Gate-drain charge | Q_{gd} | | - | 53 | -- | |

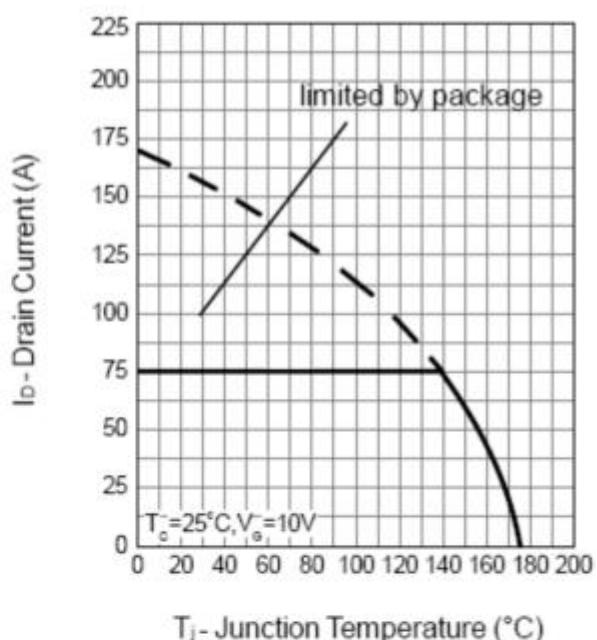
Note*:Pulse test;pulse width $\leq 300\text{us}$ duty cycle $\leq 2\%$.

8. Test circuits and waveforms

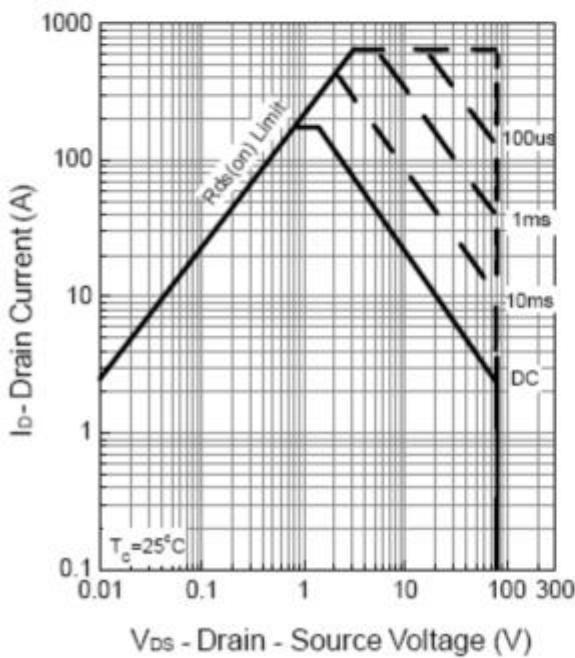
Power Dissipation



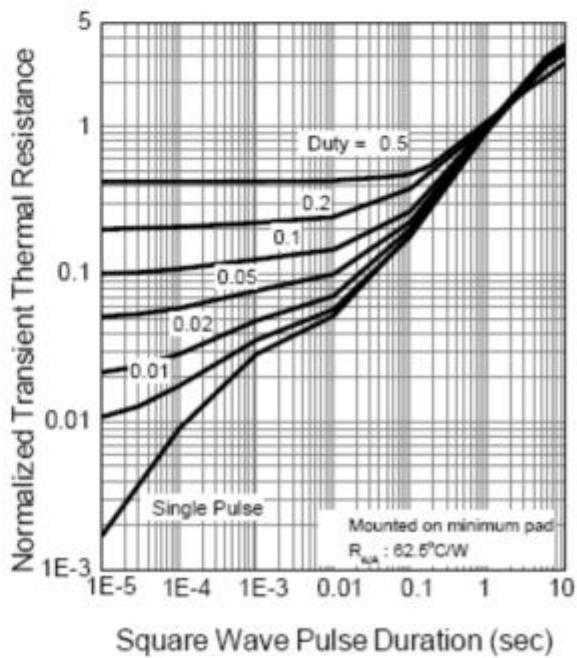
Drain Current



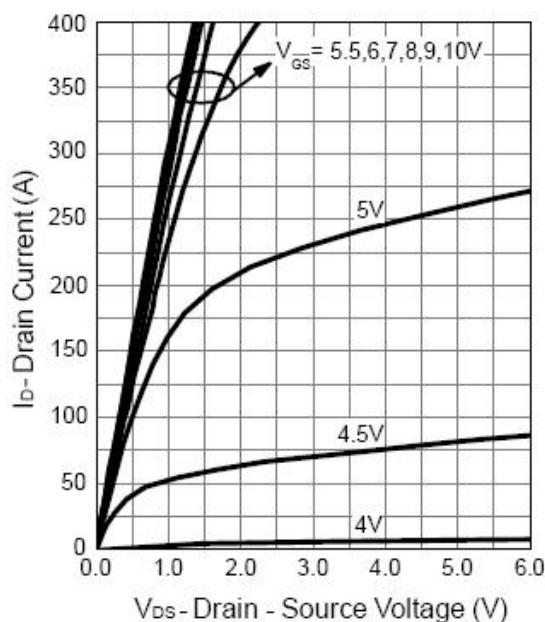
Safe Operation Area



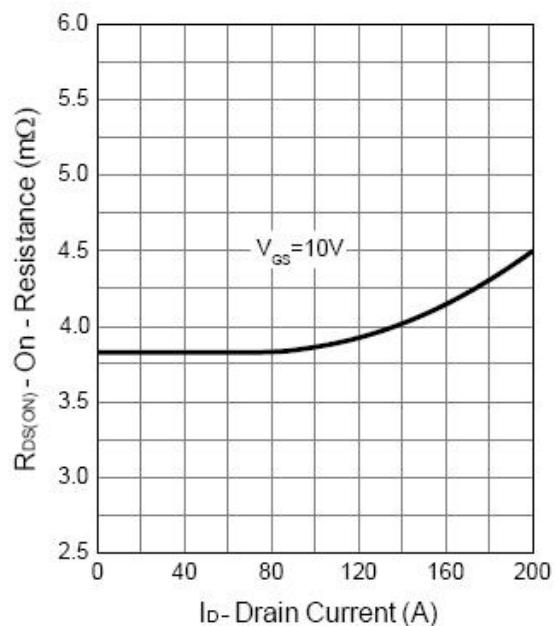
Thermal Transient Impedance



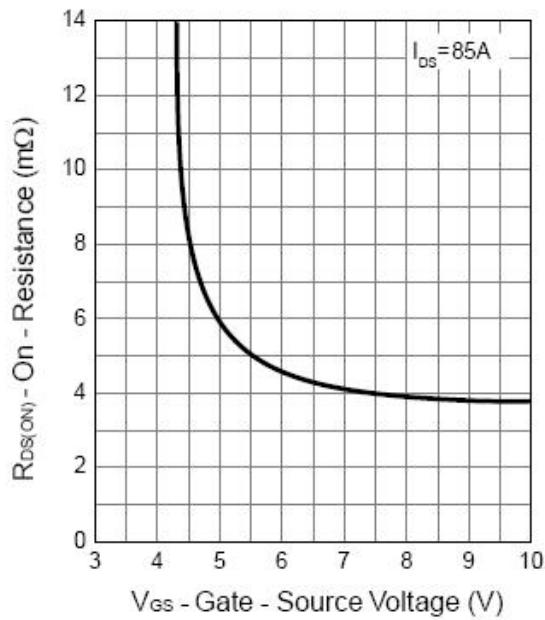
Output Characteristics



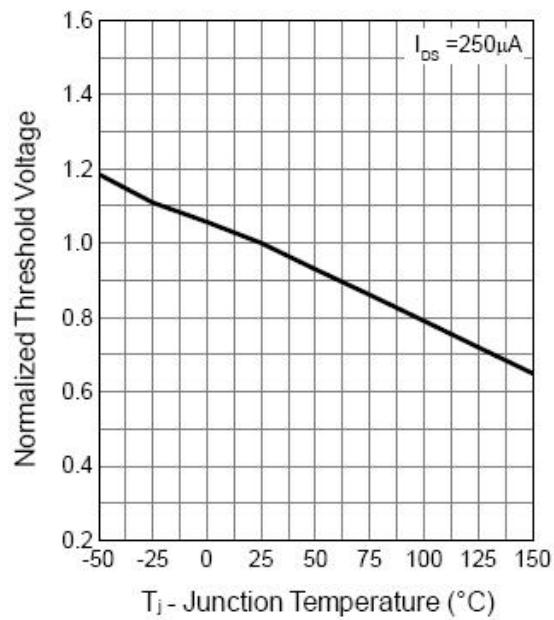
Drain-Source On Resistance



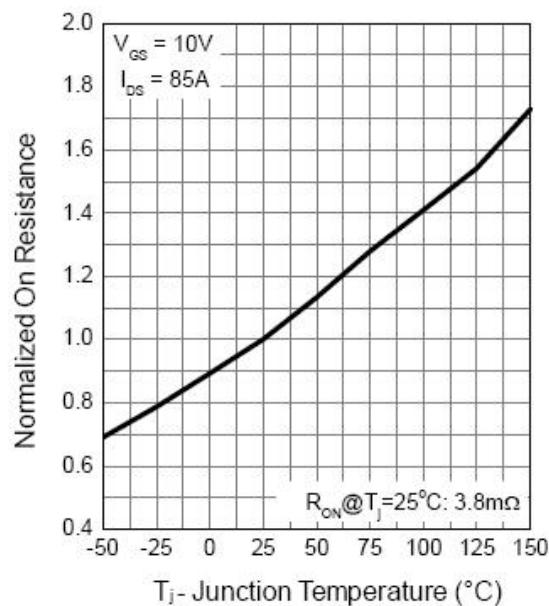
Gate-Source On Resistance



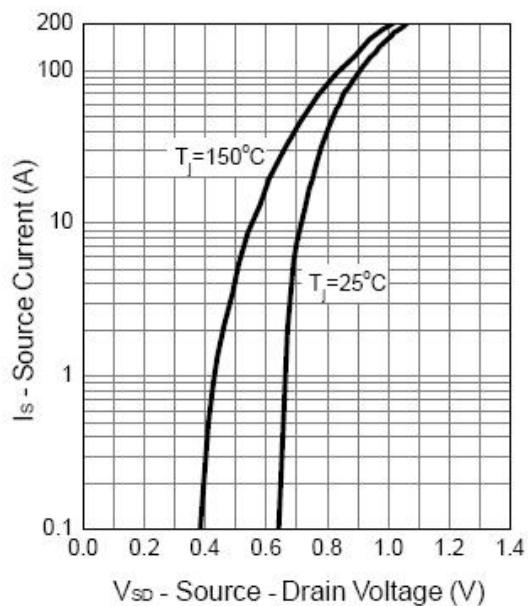
Gate Threshold Voltage



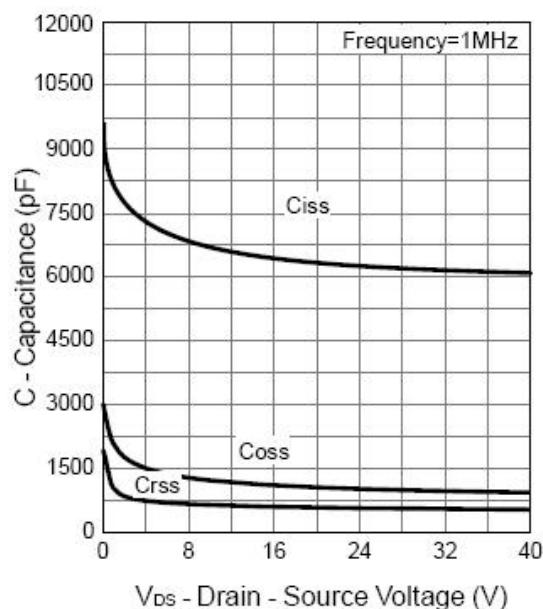
Drain-Source On Resistance



Source-Drain Diode Forward



Capacitance



Gate Charge

