

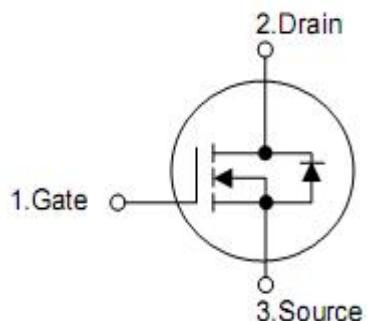
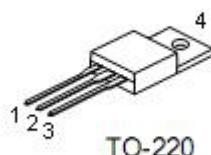
1. General Features

- $R_{DS(ON),typ.}=4m\Omega$ (typ.)@ $V_{GS}=10V$
- Proprietary New Trench Technology
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

2. Applications

- DC-DC converters
- DC-DC Inverters
- Power Supply

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

4. Ordering Information

Part Number	Package	Brand
KNP2404N	TO-220	KIA

5. Absolute maximum ratings

(T _c = 25 °C , unless otherwise specified)			
Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain-to-Source Voltage ^[1]	40	V
V _{GSS}	Gate-to-Source Voltage	±20	
I _D	Continuous Drain Current ^[2]	190	A
	Continuous Drain Current ^[3]	80	
I _{DM}	Pulsed Drain Current at V _{GS} =10V ^[2,4]	480	
E _{AS}	Single Pulse Avalanche Energy	1200	mJ
dv /dt	Peak Diode Recovery dv/dt ^[3]	5.0	V/ns
P _D	Power Dissipation	333	W
	Derating Factor above 25 °C	2	W/ °C
T _L T _{PAK}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	300 260	°C
T _J &T _{STG}	Operating and Storage Temperature Range	-55 to 175	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

* Drain current limited by maximum junction temperature.

6. Thermal characteristics

Symbol	Parameter	Ratings	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.45	°C /W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62	

6. Electrical characteristics

OFF Characteristics		(TJ=25°C,unless otherwise specified)				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-to-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40	--	--	V
I _{DSS}	Drain-to-Source Leakage Current	V _{DS} =40V, V _{GS} =0V	--	--	1	uA
		V _{DS} =32V, V _{GS} =0V, T _J =125°C	--	--	100	
I _{GSS}	Gate-to-Source Leakage Current	V _{GS} =+20V, V _{DS} =0V	--	--	+100	nA
		V _{GS} =-20V, V _{DS} =0V	--	--	-100	
ON Characteristics		(TJ=25°C,unless otherwise specified)				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
R _{DS(ON)}	Static Drain-to-Source On-Resistance ^[4]	V _{GS} =10V, I _D =80A ^[5]	--	4.0	5.0	mΩ
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2.0	--	4.0	V
Dynamic Characteristics		Essentially independent of operating temperature				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, f=1.0MHZ	--	3850	--	pF
C _{oss}	Output Capacitance		--	1750	--	
C _{rss}	Reverse Transfer Capacitance		--	420	--	
R _g	Gate Series Resistance	f=1.0MHZ	--	1.5	--	Ω
Q _g	Total Gate Charge	V _{DD} =20V, I _D =80A, V _{GS} =0 to 10V	--	97	--	nC
Q _{gs}	Gate-to-Source Charge		--	18	--	
Q _{gd}	Gate-to-Drain (Miller) Charge		--	37	--	
Resistive Switching Characteristics		Essentially independent of operating temperature				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t _{d(ON)}	Turn-on Delay Time	V _{DD} =20V, I _D =50A, V _{GS} = 10V R _G =10Ω	--	32	--	nS
t _{rise}	Rise Time		--	90	--	
t _{d(OFF)}	Turn-Off Delay Time		--	100	--	
t _{fall}	Fall Time		--	72	--	
Source-Drain Body Diode Characteristics		(TJ=25°C,unless otherwise specified)				
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I _{SD}	Continuous Source Current ^[4]	Integral PN-diode in MOSFET	--	--	190	A
I _{SM}	Pulsed Source Current ^[4]		--	--	480	
V _{SD}	Diode Forward Voltage	I _S =80A, V _{GS} =0V	--	--	1.3	V
t _{rr}	Reverse recovery time	V _{GS} =0V ,I _F =80A, dI/dt=100A/μs	--	76	--	ns
Q _{rr}	Reverse recovery charge		--	35	--	nC

Note:

- [1] T J =+25 °C to +175 °C.
- [2] [2] Silicon limited current only.
- [3] [3] Package limited current.

[4] Repetitive rating; pulse width limited by maximum junction temperature.

[5] Pulse width≤380μs; duty cycle≤2%.

7. Test circuits and waveforms

Figure 1. Maximum Transient Thermal Impedance

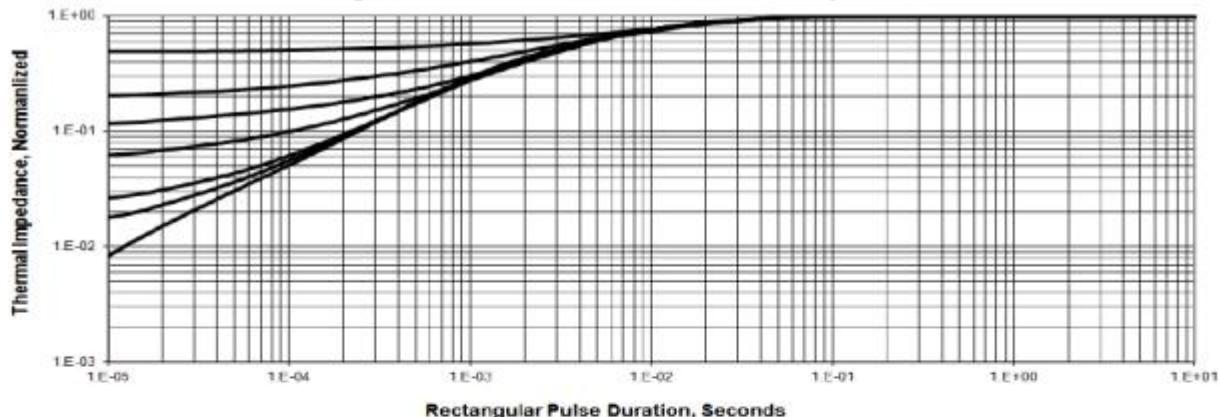


Figure 2 . Max. Power Dissipation vs Case Temperature

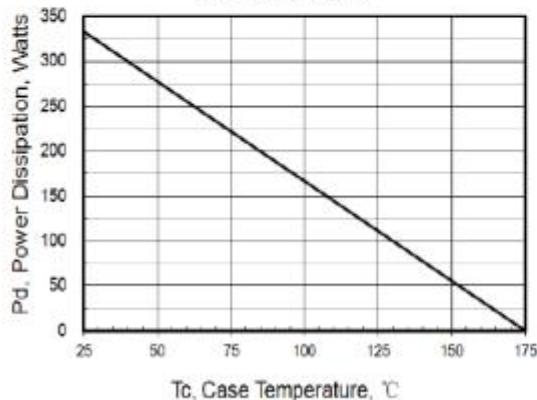


Figure 4. Typical Output Characteristics

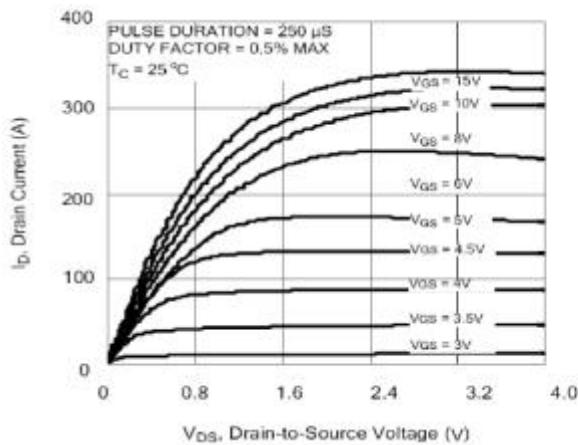


Figure 3 .Maximum Continuous Drain Current vs Tc

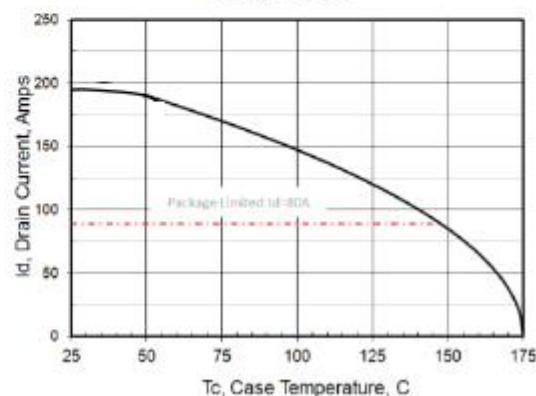


Figure5. Typical Drain-to-Source ON Resistanc vs Gate Voltage and Drain Current

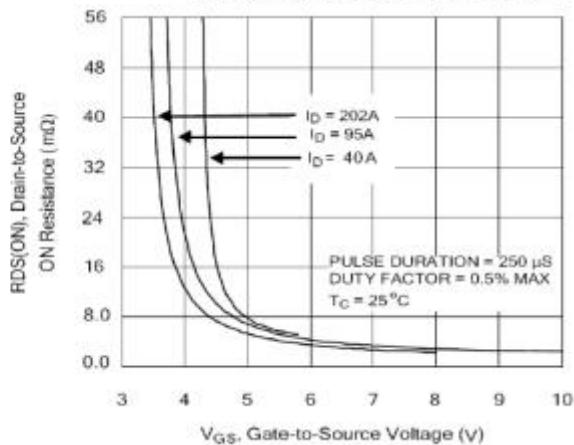


Figure 6. Peak Current Capability

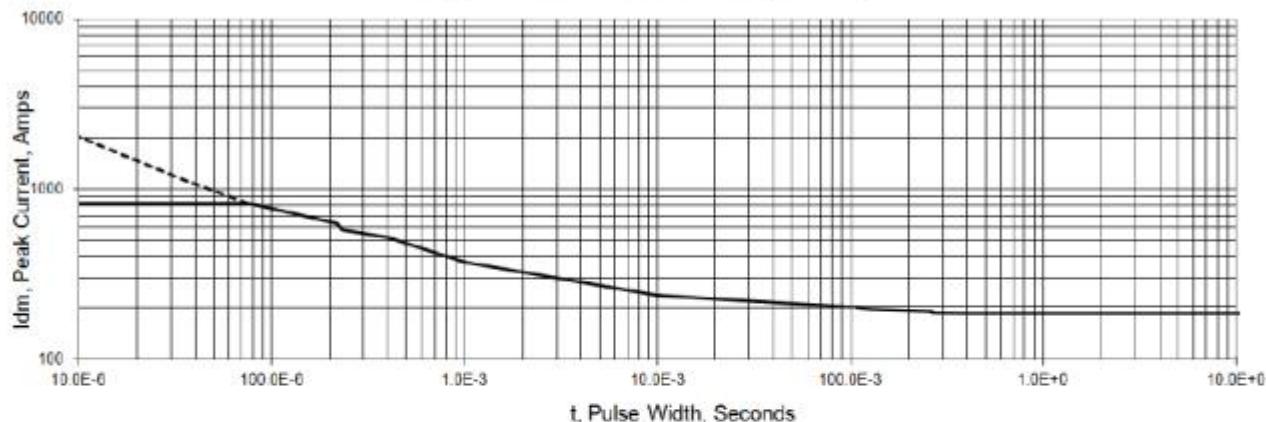


Figure 7. Typical Transfer Characteristics

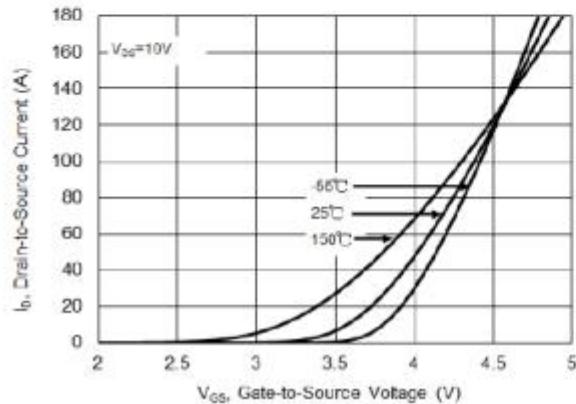


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

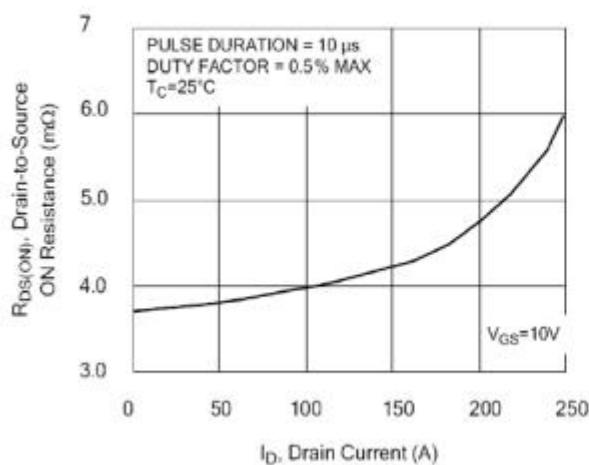


Figure 8. Unclamped Inductive Switching Capability

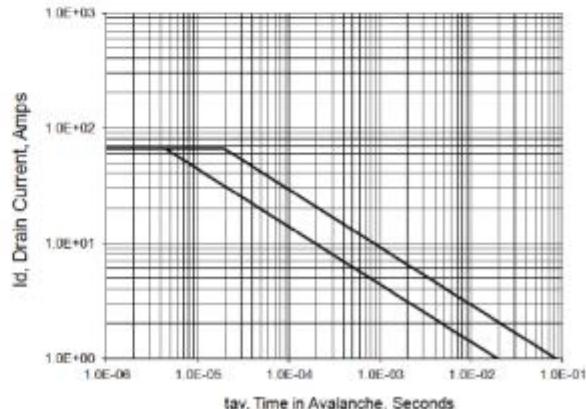


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature

