

1. Description

The KIA30N03B is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The KIA30N03B meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

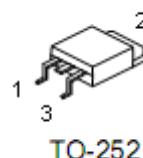
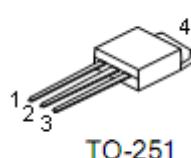
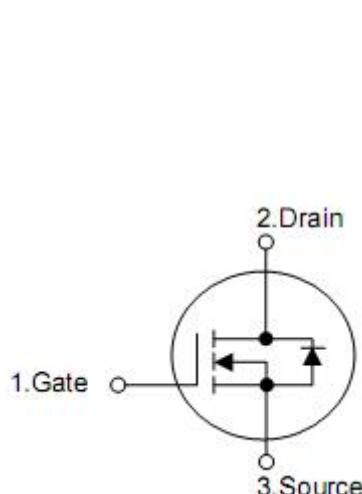
2. Features

- $R_{DS(on)} = 15\text{m}\Omega$ @ $V_{DS} = 30\text{V}$
- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

3. Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

4. Symbol



Pin	Function
1	Gate
2	Drain
3	Source

5. Absolute maximum ratings

Parameter	Symbol	Rating	Units
Drain-source voltage	V_{DS}	30	V
Gate-source voltage	V_{GS}	+20	V
Continuous drain current, $V_{GS} @ 10V^1$	I_D	30	A
$T_C=100^\circ C$		18	A
Pulsed drain current ²	I_{DM}	60	A
Single pulse avalanche energy ³	E_{AS}	72	mJ
Avalanche current	I_{AS}	21	A
Total power dissipation ⁴	P_D	25	W
Operation junction temperature range	T_J	-55 to150	°C
Storage temperature range	T_{STG}	-55 to150	°C

6. Thermal characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal resistance,Junction-ambient($t \leq 10s$) ¹	$R_{\theta JA}$	--	25	°C/W
Thermal resistance,Junction-ambient (Steady State) ¹	$R_{\theta JA}$	--	62	°C/W
Thermal resistance,Junction-case ¹	$R_{\theta JC}$	--	5	°C/W

7. Electrical characteristics

($T_J=25^\circ\text{C}$,unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
BV_{DSS} temperature coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Reference to 25°C , $I_{\text{D}}=1\text{mA}$		0.023		$\text{V}/^\circ\text{C}$
Static drain-source on-resistance ²	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$		15	18	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$		22	30	
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.5	2.5	V
$V_{\text{GS}(\text{th})}$ temperature coefficient	$\Delta V_{\text{GS}(\text{th})}$			-5.2		$\text{mV}/^\circ\text{C}$
Drain-source leakage current	I_{DSS}	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			1	μA
		$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=55^\circ\text{C}$			5	μA
Gate- source leakage current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$			± 100	nA
Forward transconductance	g_{fs}	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=10\text{A}$		10		S
Gate resistance	R_g	$V_{\text{DS}}=24\text{V},$ $V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		2.5	5	Ω
Total gate charge(4.5V)	Q_g	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=4.5\text{V}$ $I_{\text{D}} =12\text{A}$	-	7.2		nC
Gate-source charge	Q_{gs}			1.4		
Gate-drain charge	Q_{gd}			2.2		
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=12\text{V}, I_{\text{D}}=5\text{A},$ $R_G=3.3\Omega, V_{\text{GS}}=10\text{V}$		4.1		ns
Rise time	t_r			9.8		
Turn-off delay time	$t_{\text{d}(\text{off})}$			15.5		
Fall time	t_f			6.0		
Input capacitance	C_{iss}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$		572		pF
Output capacitance	C_{oss}			81		
Reverse transfer capacitance	C_{rss}			65		
Single pulse avalanche energy ⁵	EAS	$V_{\text{DD}}=25\text{V}, L=0.1\text{mH},$ $I_{\text{AS}}=10\text{A}$	16			mJ
Continuous source current ^{1,6}	I_s	$V_G= V_{\text{D}}=0\text{V},$ Force current			30	A
Pulsed source current ^{2,6}	I_{SM}				60	A
Diode forward voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=15\text{A}, T_J=25^\circ\text{C}$			1.2	V

Note:1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.

2.The data tested by pulsed, pulse width $\leq 300\mu\text{s}$,duty cycle $\leq 2\%$

3.The EAS data shows Max.rating.The test condition is $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=21\text{A}$

4.The power dissipation is limited by 150°C junction temperature

5.The Min, value is 100% EAS tested guarantee.

6.The data is theoretically the same as I_{D} and I_{DM} ,in real applications, should be limited by total power dissipation.

8. Test circuits and waveforms

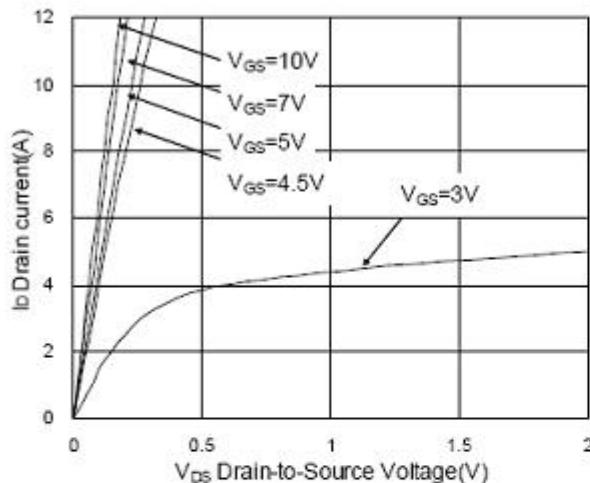


Fig.1 Typical Output Characteristics

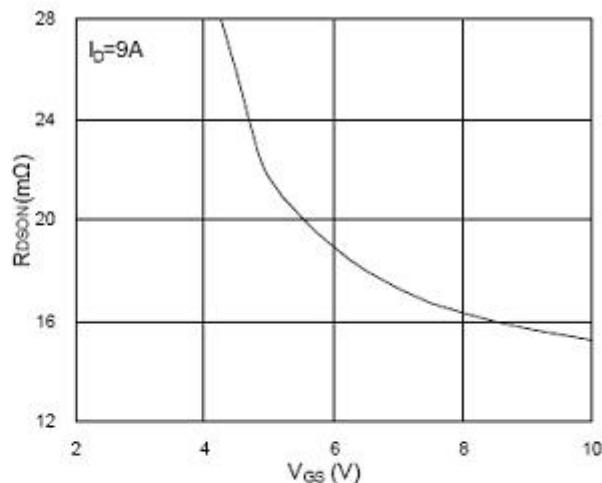


Fig.2 On-Resistance v.s Gate-Source

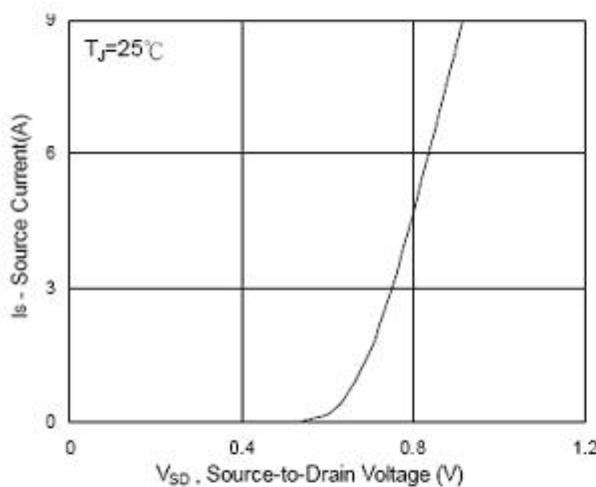


Fig.3 Forward Characteristics of Reverse

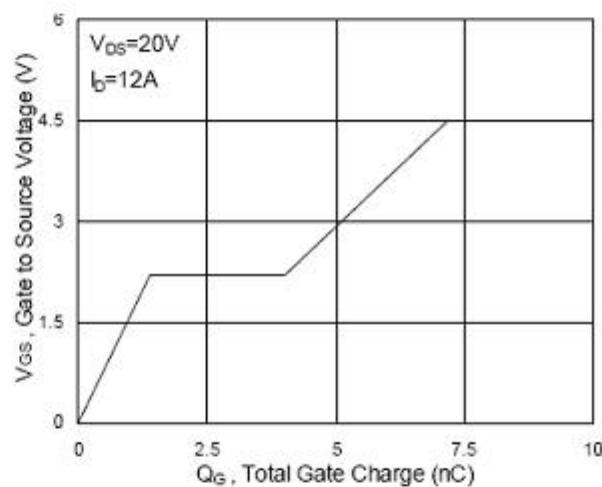


Fig.4 Gate-Charge characteristics

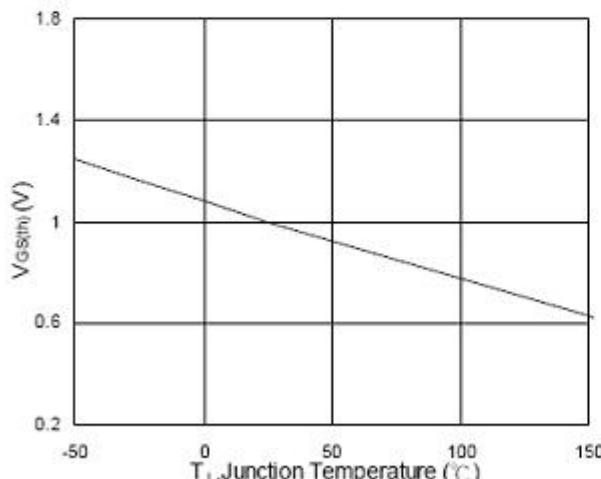


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

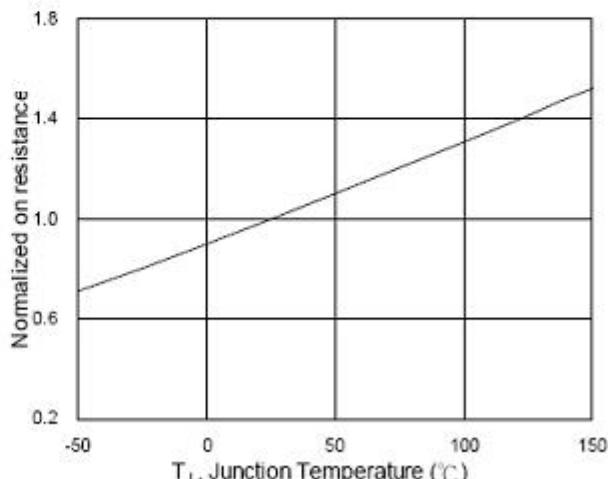


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

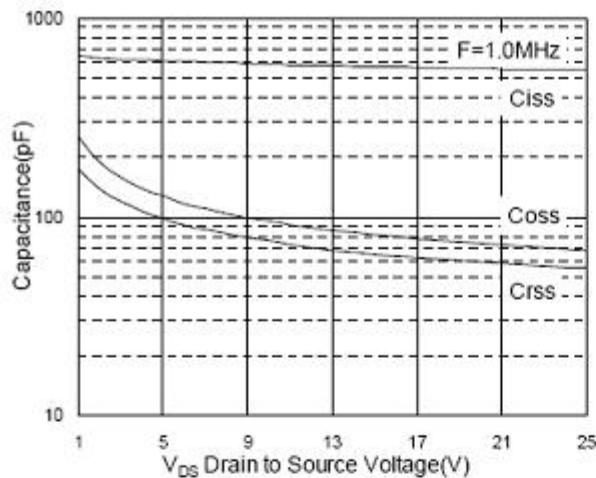


Fig.7 Capacitance

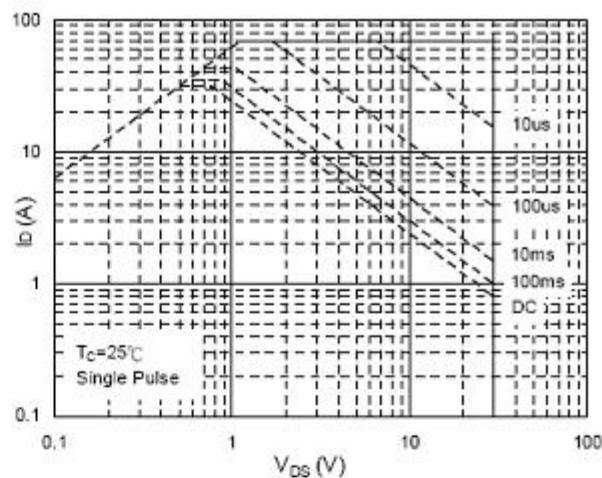


Fig.8 Safe Operating Area

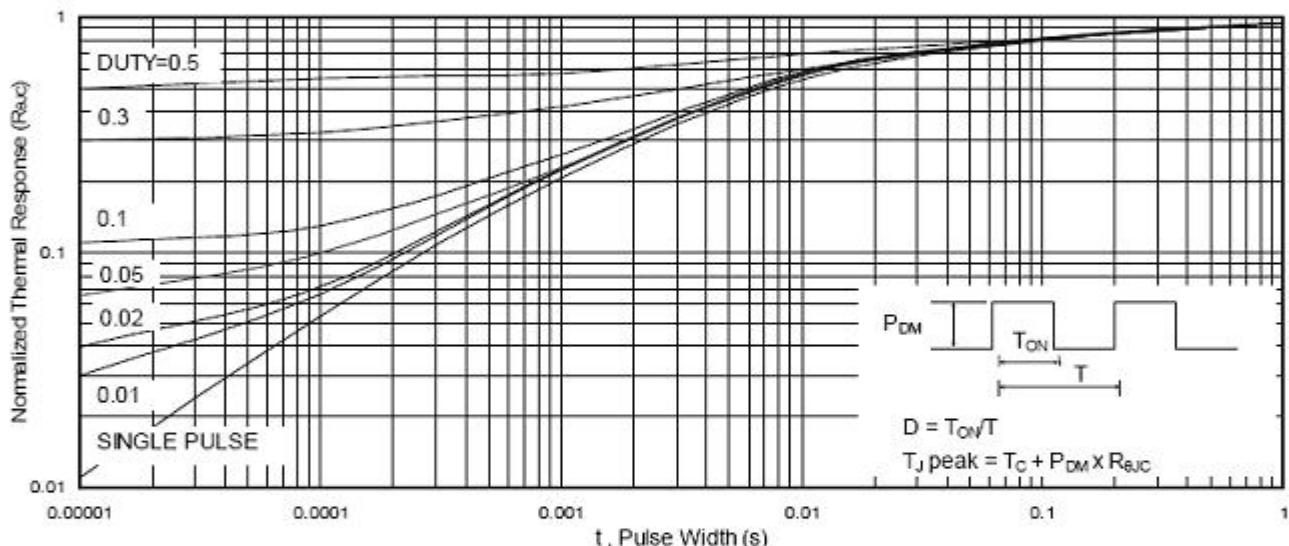


Fig.9 Normalized Maximum Transient Thermal Impedance

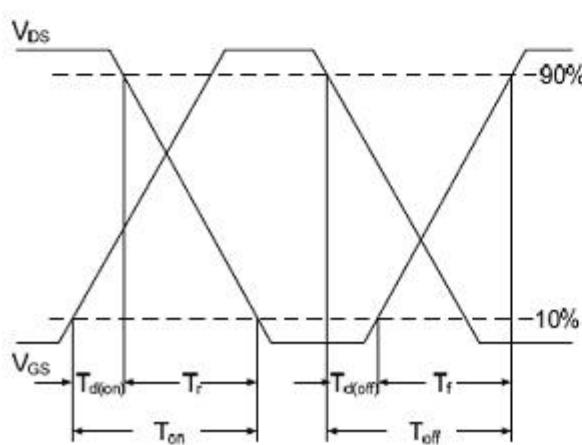


Fig.10 Switching Time Waveform

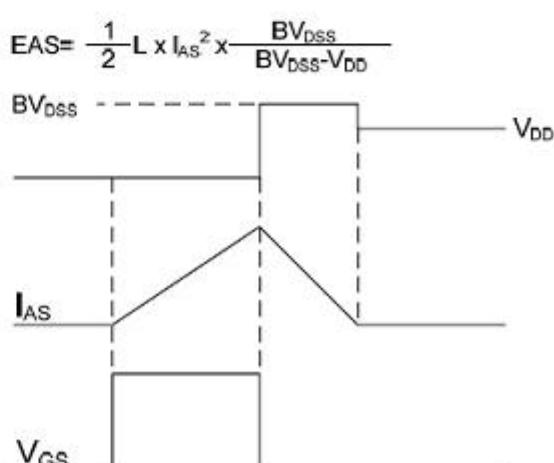


Fig.11 Unclamped Inductive Switching Waveform