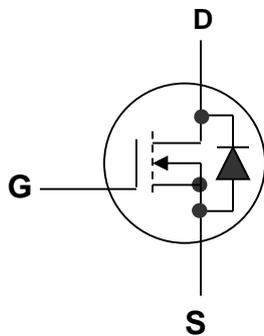
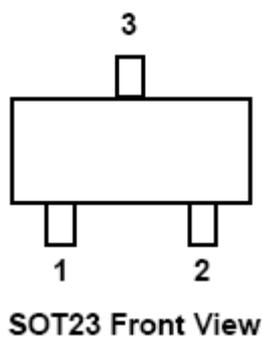


1. Features

- n $V_{DS}=20V, R_{DS(on)}=30m\Omega @ V_{GS}=10V, I_D=6.0A$
- n $V_{DS}=20V, R_{DS(on)}=40m\Omega @ V_{GS}=4.5V, I_D=3.0A$
- n $V_{DS}=20V, R_{DS(on)}=55m\Omega @ V_{GS}=2.5V, I_D=2.0A$

2.Symbol



Pin	Function
1	Gate
2	Source
3	Drain

3. Absolute maximum ratings

($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GS}	± 10	V
Drain current continuous * $T_J=125^\circ\text{C}$	I_D	6.0	A
Drain current pulsed	I_{DM}	20	
Power dissipation*	P_D	1.25	W
Thermal resistance, junction-ambient	R_{thJA}	100	$^\circ\text{C/W}$
Junction and storage temperature range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

*Surface mounted on FR 4 board, $t_{\leq 10}$ sec.

4. Electrical characteristics

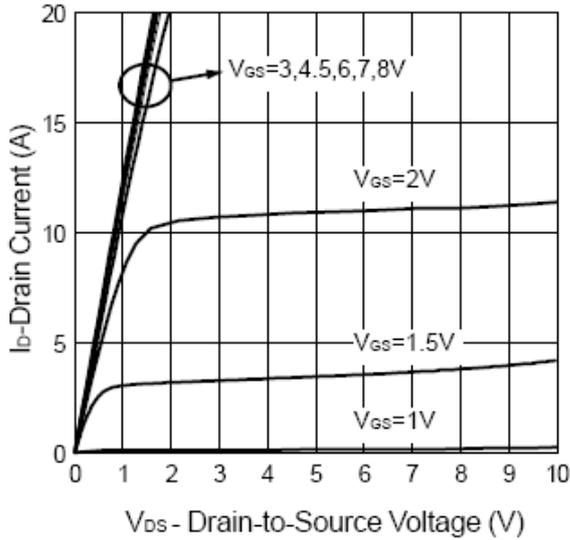
($T_A=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	20	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=16V, V_{GS}=0V$	-	-	1	μA
Gate- body leakage	I_{GSS}	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	± 100	nA
Gate threshold voltage*	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.78	1.0	V
On state drain current	$I_{D(on)}$	$V_{DS}=5V, V_{GS}=4.5V$	5	-	-	A
Static drain-source on-resistance*	$R_{DS(on)}$	$V_{GS}=10V, I_D=6.0A$	-	28	30	m Ω
		$V_{GS}=2.5V, I_D=3.0A$	-	38	40	
		$V_{GS}=1.8V, I_D=2.0A$	-	52	55	
Forward transconductance*	g_{fs}	$V_{DS}=15V, I_D=5A$	30	-	-	S
Input capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V,$ $f=1\text{MHz}$	-	888	-	pF
Output capacitance	C_{oss}		-	144	-	
Reverse transfer capacitance	C_{rss}		-	115	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=10V, I_D=1.0A,$ $R_L=10\Omega, R_G=6\Omega,$ $V_{GS}=4.5V$	-	31.8	-	ns
Rise time	t_r		-	14.5	-	
Turn-off delay time	$t_{d(off)}$		-	50.3	-	
Fall time	t_f		-	31.9	-	
Total gate charge	Q_g	$V_{DS}=10V, V_{GS}=4.5V$ $I_D=3.5A$	-	16.8	-	nC
Gate-source charge	Q_{gs}		-	2.5	-	
Gate-drain charge	Q_{gd}		-	5.4	-	
Diode forward voltage	V_{SD}	$V_{GS}=0V, I_S=1.25A$	-	0.825	1.3	V
Drain-source diode forward current*	I_S		-	-	1.25	A

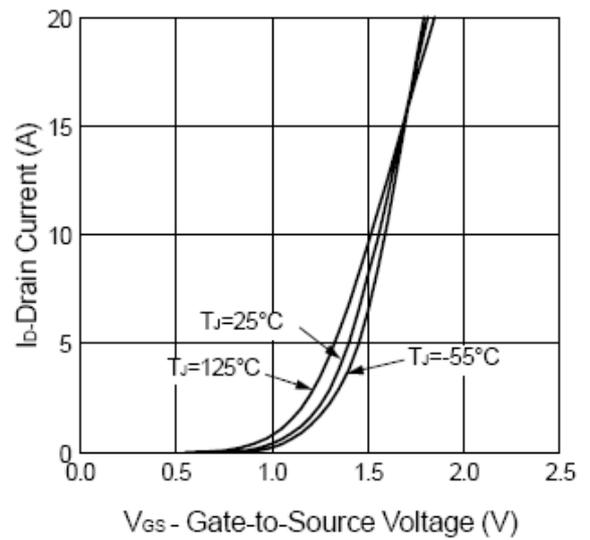
*Pulsed test:pulse width $\leq 300\mu s$,duty cycle $\leq 2\%$

5. Test circuits and waveforms

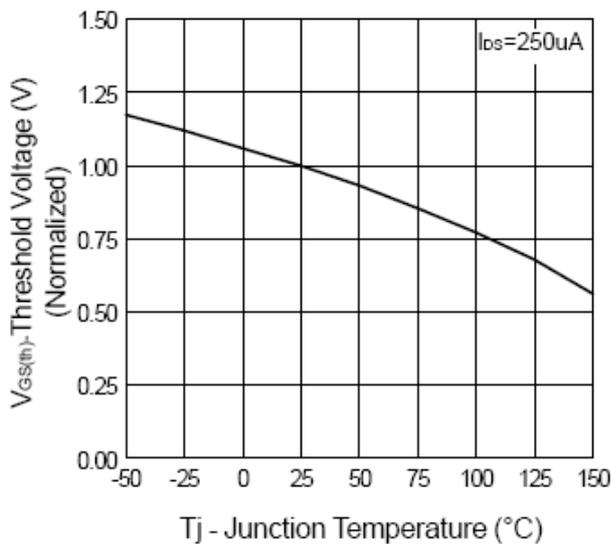
Output Characteristics



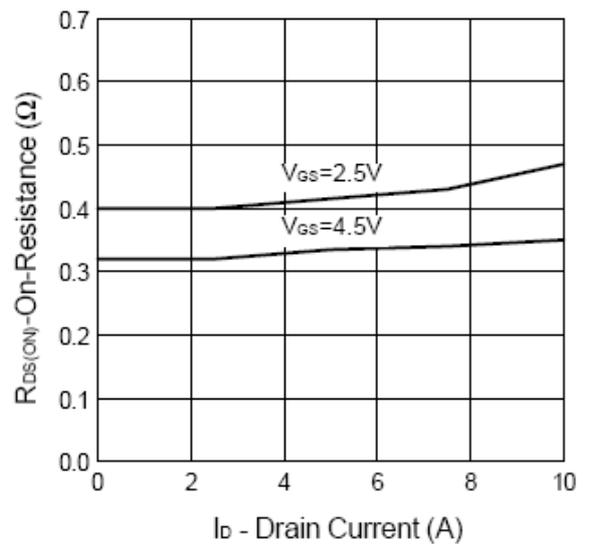
Transfer Characteristics



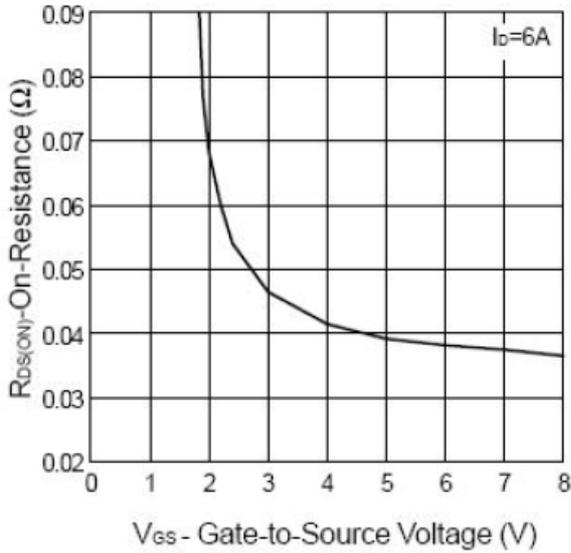
Threshold Voltage vs. Junction Temperature



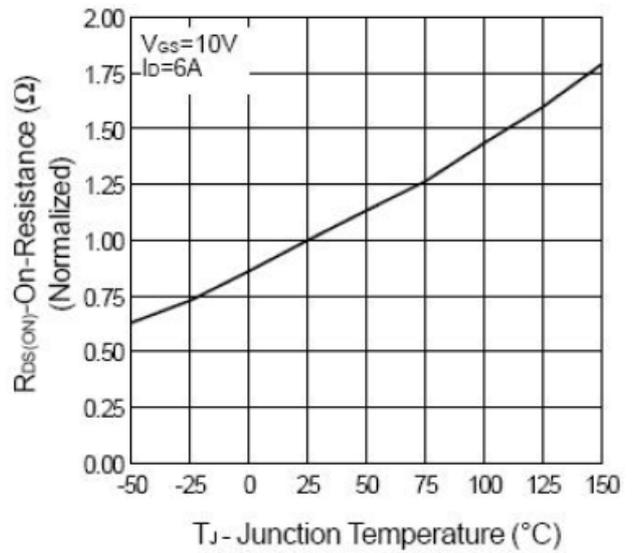
On-Resistance vs. Drain Current



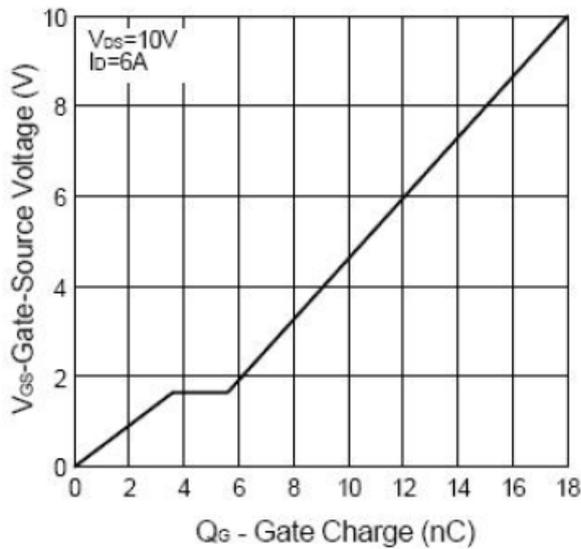
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature



Gate Charge



Capacitance

