

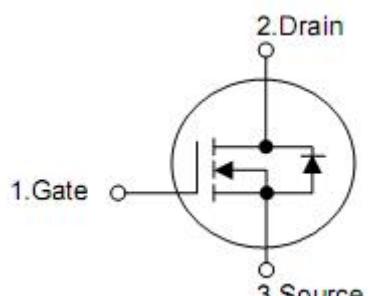
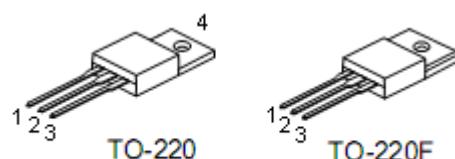
1. Description

The KNX6165A-N-Channel enhancement mode silicon gate power MOSFET is designed for high voltage, high speed power switching applications such as high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology

2. Features

- ROHS Compliant
- $R_{DS(ON),typ}=0.6\ \Omega @ V_{GS}=10V$
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

4. Ordering Information

Part Number	Package	Brand
KNF6165A	TO-220F	KIA
KNP6165A	TO-220	KIA

5. Absolute maximum ratings

TC=25°C unless otherwise specified

Parameter	Symbol	Ratings		Unit
		TO220	TO22F	
Drain-to-Source Voltage	V _{DSS}	650		V
Gate-to-Source Voltage	V _{GSS}	±20		
Continuous Drain Current	I _D	10		A
Pulsed Drain Current at V _{GS} =10V	I _{DM}	40		
Single Pulse Avalanche Energy	E _{AS}	800		mJ
Power Dissipation	P _D	216	50	W
Derating Factor above 25 °C		1.72	0.4	W/ °C
Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	T _L T _{PAK}	300		°C
Operating and Storage Temperature Range	T _J & T _{STG}	-55 to 150		

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

6. Thermal characteristics

Parameter	Symbol	Ratings		Units
		TO220	TO220F	
Thermal resistance, junction-ambient	R _{θJA}	62	100	°C/W
Thermal resistance, Junction-case	R _{θJC}	0.58	2.5	

7. Electrical characteristics

($T_J=25^\circ\text{C}$, unless otherwise notes)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Off characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	650	-	-	V
Drain-to-source Leakage Current	I_{DSS}	$V_{\text{DS}}=650, V_{\text{GS}}=0\text{V}$	-	-	1	μA
		$V_{\text{DS}}=520, V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$,	-	-	100	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}}=20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	+1.0	nA
		$V_{\text{GS}}=-20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	-1.0	nA
On characteristics						
Static drain-source on-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=5\text{A}$	-	0.6	0.9	Ω
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0	-	4.0	V
Forward Transconductance	g_{fs}	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=5\text{A}$	-	10	-	S
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$	-	1554	-	pF
Output capacitance	C_{oss}		-	153	-	pF
Reverse transfer capacitance	C_{rss}		-	15	-	pF
Total gate charge						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=325, I_{\text{D}}=10\text{A},$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=9.1\Omega$	-	15	-	ns
Rise time	t_r		-	25	-	ns
Turn-off delay time	$t_{\text{d}(\text{off})}$		-	51	-	ns
Fall time	t_f		-	31	-	ns
Total gate charge	Q_g		-	39	-	nC
Gate-source charge	Q_{gs}		-	7.0	-	nC
Gate-drain charge	Q_{gd}		-	16	-	nC
Drain-source diode characteristics						
Drain-source diode forward voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=10\text{A}$	-	-	1.5	V
Continuous drain-source current [2]	I_{SD}	Integral pn-diode In MOSFET	-	-	10	A
Pulsed drain-source current [2]	I_{SM}		-	-	40	A
Reverse recovery time	t_{rr}	$V_{\text{GS}}=0\text{V}, I_{\text{F}}=10\text{A}$ $dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	273	-	ns
Reverse recovery charge	Q_{rr}		-	1.7	-	μC

Note: [1] $T_J=+25^\circ\text{C}$ to $+150^\circ\text{C}$

[2] Pulse width $\leq 380\mu\text{s}$; duty cycle $\leq 2\%$.

8. Typical Characteristics

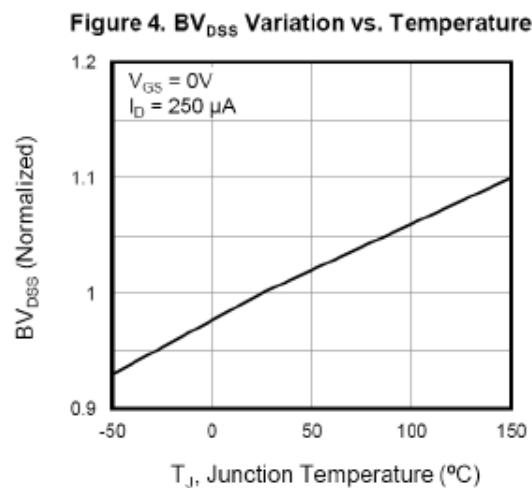
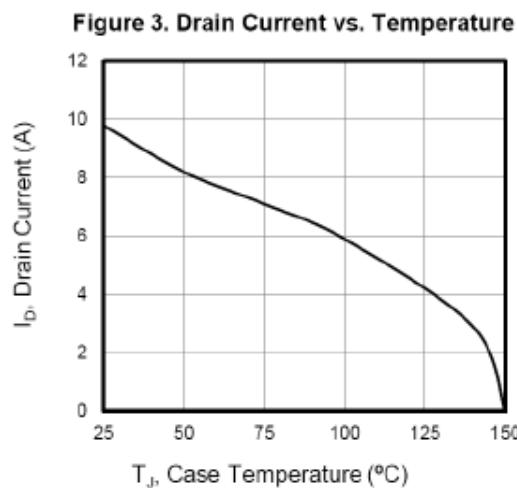
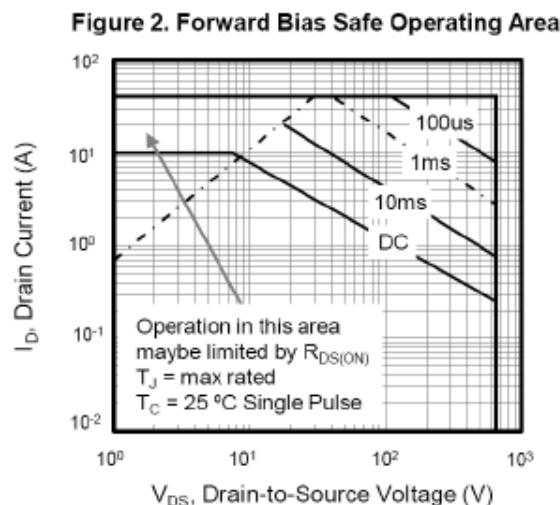
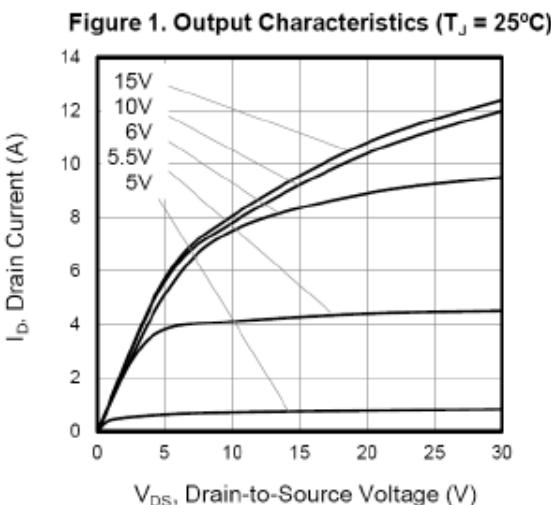


Figure 7. Capacitance

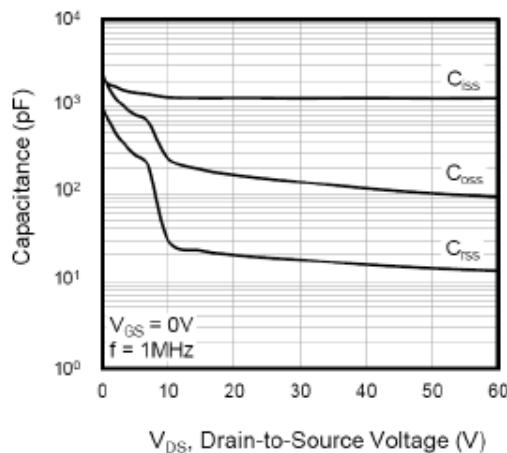


Figure 8. Gate Charge

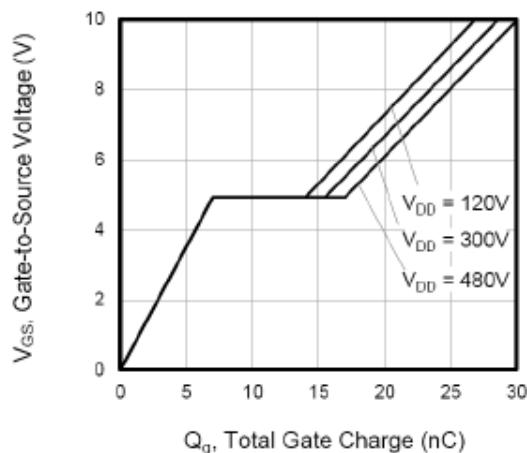


Figure 9. Body Diode Forward Voltage

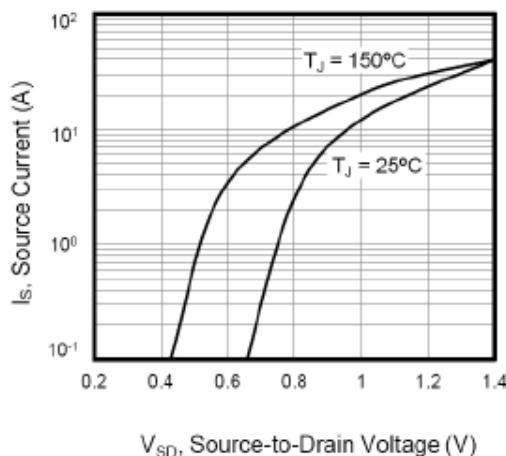
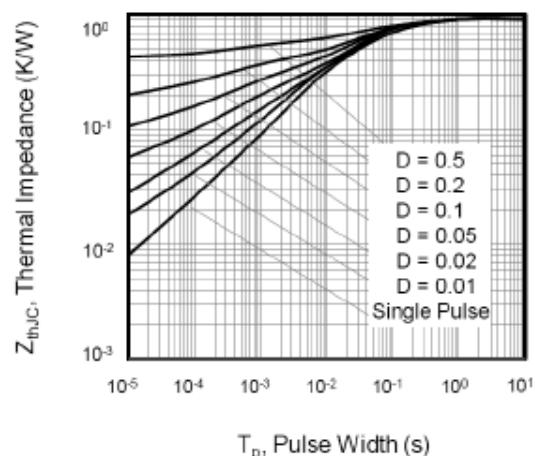


Figure 10. Transient Thermal Impedance



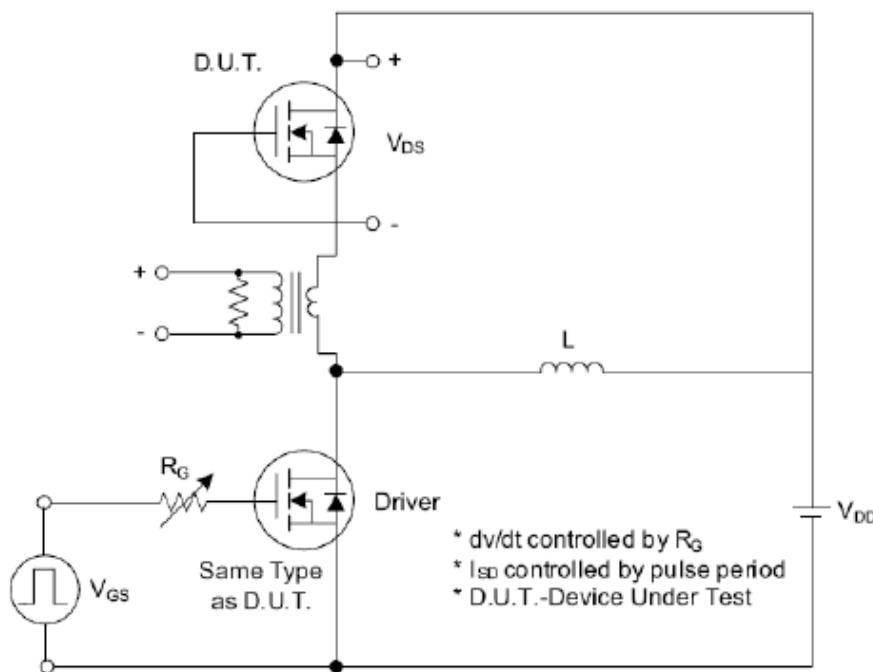


Fig. 1.1 Peak Diode Recovery dv/dt Test Circuit

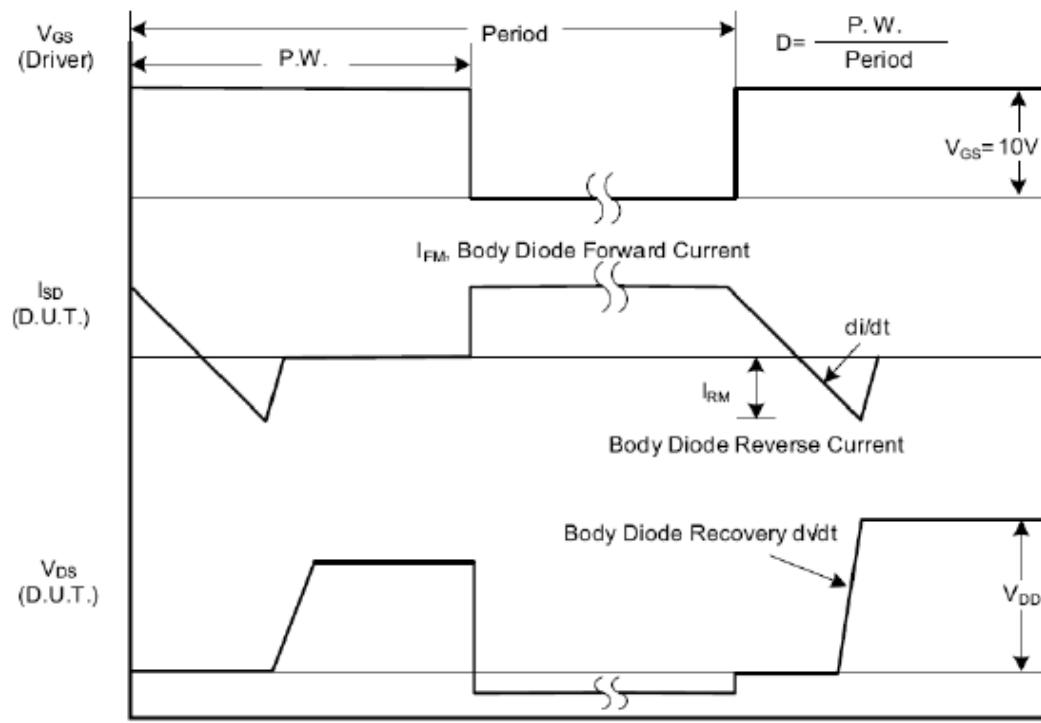


Fig. 1.2 Peak Diode Recovery dv/dt Waveforms

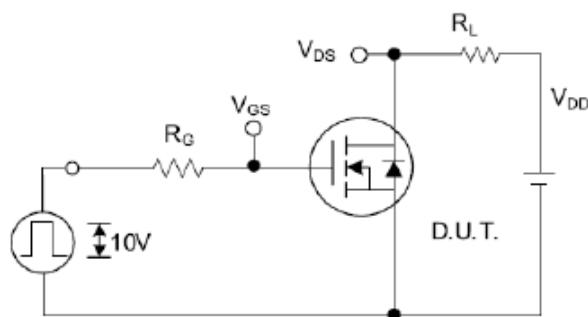


Fig. 2.1 Switching Test Circuit

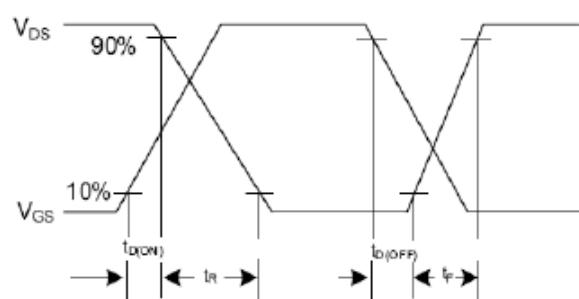


Fig. 2.2 Switching Waveforms

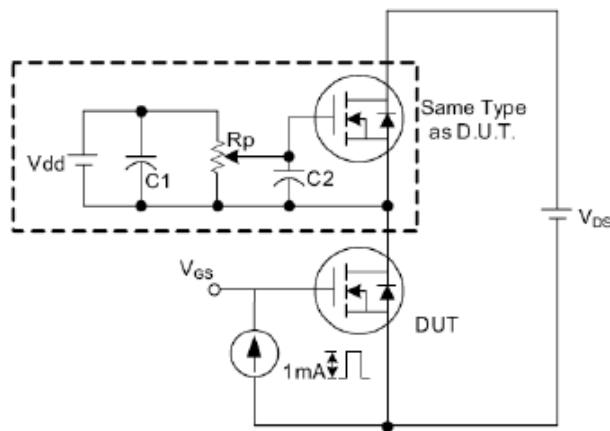


Fig. 3 . 1 Gate Charge Test Circuit

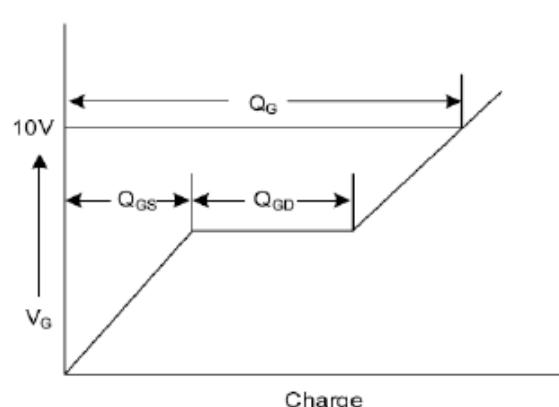


Fig. 3 . 2 Gate Charge Waveform

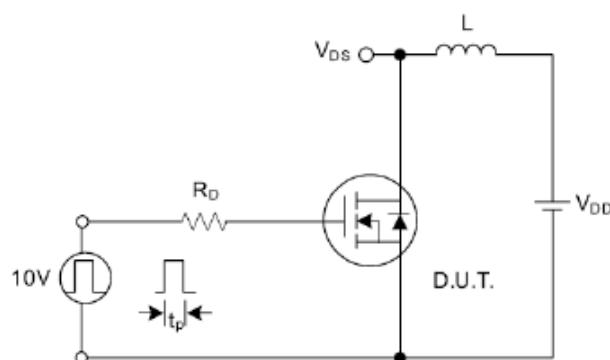


Fig. 4.1 Unclamped Inductive Switching Test Circuit

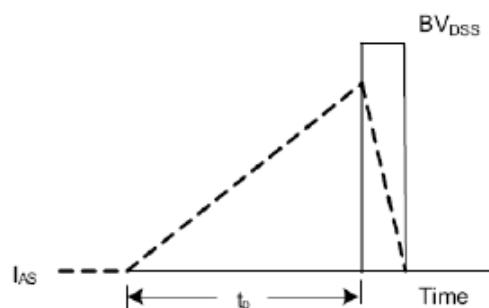


Fig. 4.2 Unclamped Inductive Switching Waveforms