

1. Description

The KIA78L15 is monolithic fixed voltage regulator integrated circuit. It is suitable for applications that required supply current up to 100mA.

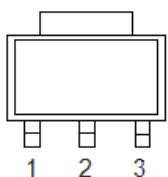
2. Features

- Output current up to 100mA
- No external part needed
- Thermal overload shutdown protection
- Short circuit current limiting
- SOT89 package

3. Applications

- Battery-powered circuitry
- Post regulator for switching power supply

4. Pinning information



SOT-89Front View

Pin	Description
1	V_{OUT}
2	GND
3	V_{IN}

5. Maximum ratings

($T_a=25^\circ\text{C}$,unless otherwise notes)

Parameter	Symbol	Rating	Units
Input voltage	V_{IN}	35	V
Power dissipation	P_D	500	mW
Junction temperature	T_J	-20~+125	$^\circ\text{C}$
Operating temperature	T_{OPR}	-20~+85	$^\circ\text{C}$
Storage temperature	T_{STG}	-65~+150	$^\circ\text{C}$

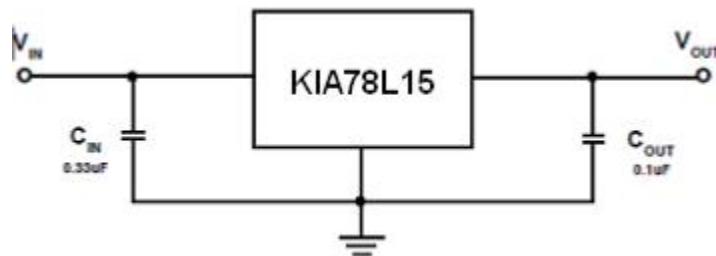
6. Electrical characteristics

($V_{IN}=23\text{V}, I_{OUT}=40\text{mA}, C_{IN}=0.33\mu\text{F}, C_{OUT}=0.1\mu\text{F}, T_J=25^\circ\text{C}$,unless otherwise notes)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Output voltage	V_{OUT}		14.40	15	15.60	V
		$17.5\text{V} \leq V_{IN} \leq 30\text{V}$ $1.0\text{mA} \leq I_{OUT} \leq 40\text{mA}$	14.25	15	15.75	V
		$1.0\text{mA} \leq I_{OUT} \leq 70\text{mA}$	13.95	15	16.05	V
Line regulation	Reg line	$17.5\text{V} \leq V_{IN} \leq 30\text{V}$	-	130	300	mV
		$20\text{V} \leq V_{IN} \leq 30\text{V}$	-	110	250	mV
Load regulation	Reg load	$1.0\text{mA} \leq I_{OUT} \leq 100\text{mA}$	-	25	150	mV
		$1.0\text{mA} \leq I_{OUT} \leq 40\text{mA}$	-	12	75	mV
Quiescent current	I_Q		-	3.1	6.5	mA
Quiescent current change	ΔI_Q	$20\text{V} \leq V_{IN} \leq 30\text{V}$	-	0.15	1.5	mA
		$1.0\text{mA} \leq I_{OUT} \leq 40\text{mA}$	-	0.08	0.1	mA
Output noise voltage	V_{ON}	$10\text{Hz} \leq f \leq 100\text{KHz}$	-	90	-	uVrm
Ripple rejection ratio	RR	$18.5\text{V} \leq V_{IN} \leq 28.5\text{V}$, $f=120\text{Hz}$	34	40	-	dB
Dropout voltage	V_D		-	1.7	-	V

Note1:The maximum steady state usable output current is dependent on input voltage,heat sinking,lead length of the package and copper patten of PCB.

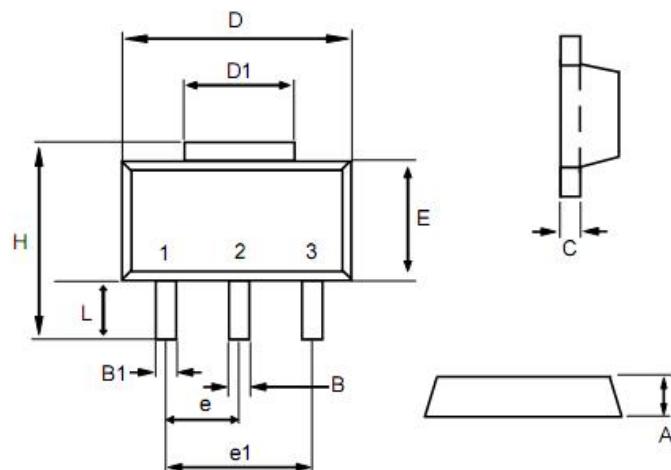
7. Application circuits



Note1:The input voltage must remain typically 1.7V above the output voltage.

Note2:Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

8. Package outline



Dim	min	max
A	1.40	1.60
B	0.40	0.56
B1	0.35	0.48
C	0.35	0.44
D	4.40	4.60
D1	1.35	1.83
e	1.50 BSC	
e1	3.00 BSC	
E	2.29	2.60
H	3.75	4.25
L	0.80	1.20

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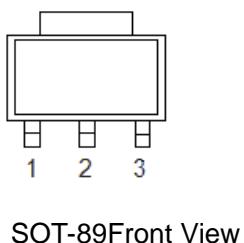
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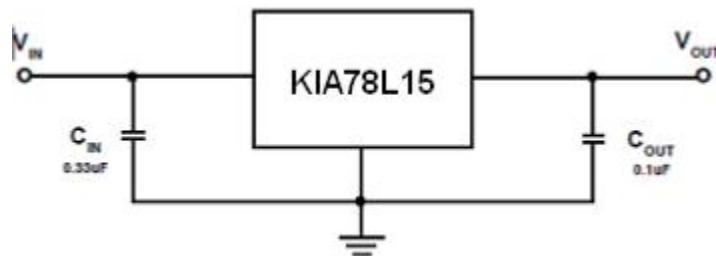
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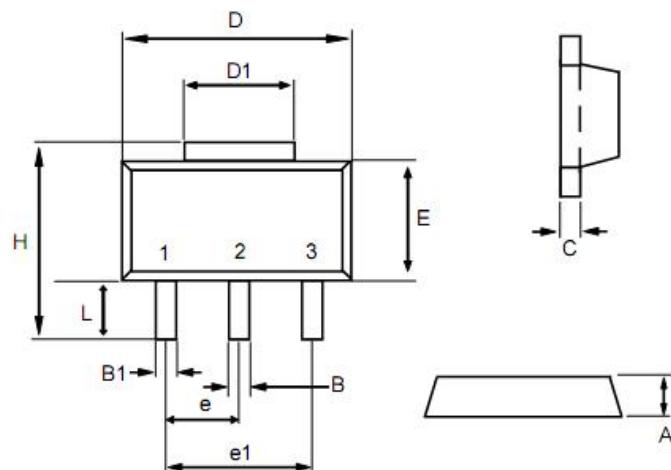
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